

PARALLELIZING ATHENA MP EVENT RECONSTRUCTION

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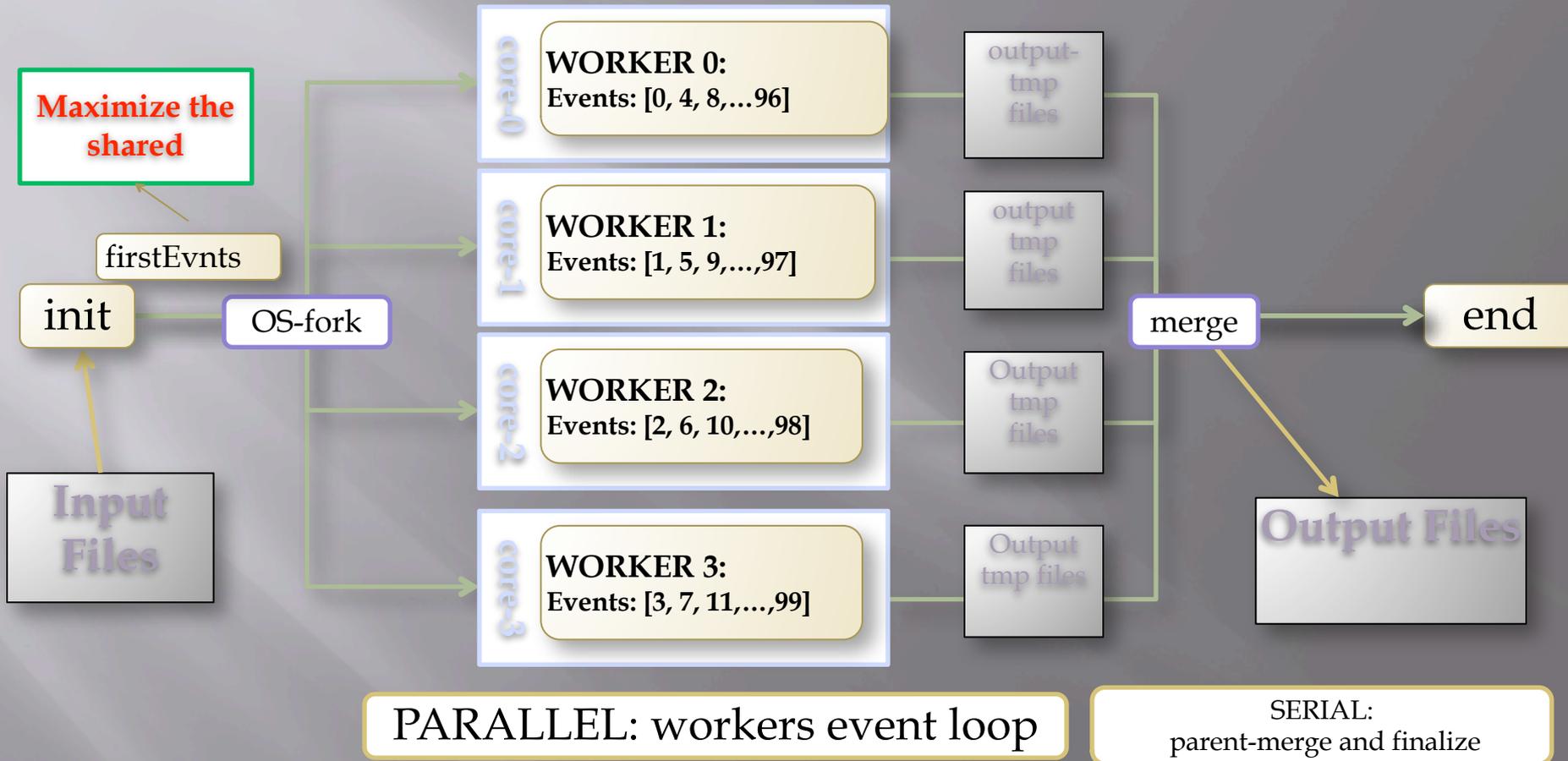
Goals

- ▣ Benchmark athenaMP on multi-core
 - Investigate application event processing event processing scalability
- ▣ Identify bottlenecks to scalability and propose strategies to mitigate them
 - Test at higher core counts than in production

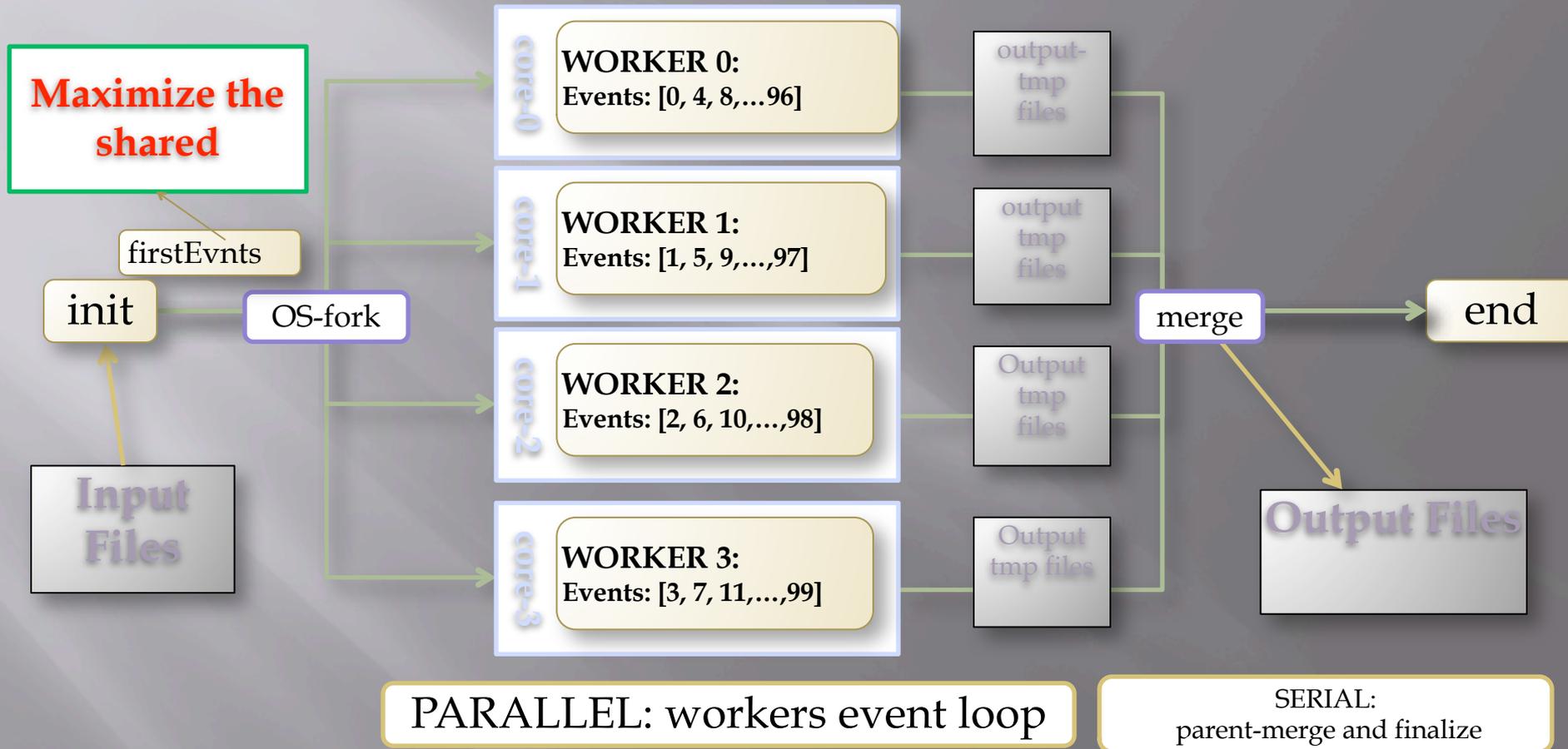
Athena MJ



Athena MP



Athena MP



Testing Platforms

Name	CPU	Cache	# CPU's	# cores	Mem
Turing	AMD Opteron 8384	L1: 128KB L2: 512KB L3: 6MB/cpu	8	32	256GB
Coors	Intel Xeon X5550	L1: 32KB (inst & data) L2: 256KB L3: 8MB/cpu	2	8	24GB
VoAtlas	Intel Xeon E5410	L1: 32KB (inst & data) L2: 32KB L3: 8MB/cpu	2	8	16GB

Tools Used

- ▣ sar: Measure number of reads and writes from physical disks, total I/O output, system load
- ▣ vmstat: Measure memory performance including, total used, paging, free memory
- ▣ IPM (Integrated Performance Monitoring) to measure total amount of time spent in I/O vs. computation
- ▣ numastat/numactl controls and reports on NUMA settings

Intel Tools

- ▣ Performance Tuning Utility (PTU)
 - Uses a Linux kernel module to provide a sampling profiler
 - Captures information from the hardware counters available on Intel chips
 - ▣ Information varies between processor families
 - Large number of instruction cache misses, relatively few data cache misses
 - Not using SSE division instructions
- ▣ Intel compilers
 - Link level optimizations
 - Can use output from PTU to optimize hot spots

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WRONG!!